



~~5~~In re application of:

Examiner: D. Kang

Art Unit: 2811

For: TRANSISTOR AND LOGIC CIRCUIT
ON THIN SILICON-ON-INSULATOR
WAFERS BASED ON GATE INDUCED
LEAKAGE CURRENTS

Box Non-Fee Amendment
Honorable Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Notice of Allowability mailed January 15, 2003, the Applicant submits the following Remarks. The Applicants respectfully request that the Examiner enter the Amendment and consider the Remarks.

It is not believed that extensions of time are required beyond those that may otherwise be provided for in documents accompanying this paper. However, if additional extensions of time are necessary to prevent abandonment of this application, then such extensions of time are hereby petitioned under 37 C.F.R. §1.136(a), and any fees required therefore are hereby authorized to be charged to Deposit Account No. 02-2666. Please credit any overpayment to the same deposit account.

Kindly consider the following Remarks.

#8 / Response
w/ Drawing
RECEIVED
APR 17 2003
FC 2800 MAIL ROOM
5/12/03
Smith